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#15: Suppl.
JDS

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 09/885,393
Filing Date June 19, 2001
Inventor Richard Holscher et al.
Assignee Micron Technology, Inc.
Group Art Unit 1756
Examiner Kathleen Duda
Attorney's Docket No. MI22-1694
Title: Semiconductor Processing Methods

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References –See Attached Form PTO-1449

The Examiner's attention is directed to the references which are listed on the attached Form PTO-1449, copies of which are attached. No admission is made regarding whether all the submitted references are prior art.

Citation of the referenced art is respectfully requested.

This Supplemental Information Disclosure Statement is being filed within three months of the filing date of the application or before the mailing date of a first Office Action, whichever occurs last. Therefore, no fee is believed to be required. However, in the event that a fee is required for filing this Supplemental Information Disclosure Statement, please charge the fee specified under 37 C.F.R. § 1.17(p) to Deposit Account No. 23-0925.

Respectfully submitted,

Dated: 2/18/03

By: [Signature]

Robert C. Hyta
Reg. No. 46,791

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